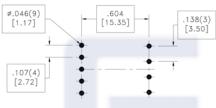


Customer to tie terminals 2+3 on PCB.



RECOMMENDED P.C. PATTERN, COMPONENT SIDE

## ELECTRICAL SPECIFICATIONS @ 25°C unless otherwise noted:

PARAMETER		TEST CONDITIONS	VALUE	
D.C. RESISTANCE	1-3	@20°C	0.045 ohms max.	
D.C. RESISTANCE	2-4	@20°C	0.035 ohms max.	
D.C. RESISTANCE	7-8	@20°C	0.085 ohms max.	
INDUCTANCE	1-4	tie(2+3), 100kHz, 100mVAC, Ls	870uH ±30%	
LEAKAGE INDUCTANCE	1-4	tie(2+3, 7+8), 100kHz, 10mVAC, Ls	400nH typ., 800nH max.	
DIELECTRIC	1-8	tie(2+3), 5000VAC, 1 second	4000VAC, 1 minute	
TURNS RATIO		(1-4):(8-7), tie(2+3)	1.307:1, ±1%	
TURNS RATIO		(2-4):(1-3)	1.125:1, ±1%	

## GENERAL SPECIFICATIONS:

Designed to comply with the following requirements as defined by IEC60950-1, EN60950-1, UL60950-1/CSA60950-1 and AS/NZS60950.1:

- Reinforced insulation for a primary circuit at a working voltage of 400VDC.

Wire insulation & RoHS status not affected by wire color. Wire insulation color may vary depending on availability.

REV.	DATE	Packaging Specifications Method: Tray PKG-0730	0
6B	2/17		CONVENTION
6A	4/12		



Tolerances unless otherwise specified: Decimals: ±.005 [.13] Footprint: ±.001 [.03] Angles: ±1° Fractions: ±1/64

This drawing is dual dimensioned. Dimensions in brackets are in millimeters.

## **TRANSFORMER**

FKS750510476

eiSos p/n: FKS750510476

